Instructor: Dr. Theresa Swift
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Office: 217 EECH
Office Hours: MTuWTh 9:45 a.m.-10:15 a.m. & by appointment
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Text: Circuit Analysis 2nd Edition, Cunningham and Stuller

Homework Policies: No Late work will be accepted. Due dates and times are posted in the daily schedule on Blackboard. If you must miss a class you must turn in HW early to receive credit. Homework will be accepted by email if you are unable to attend class. Working together on homework is encouraged; however this does not mean copying of other’s solutions. Help one another, but do your own work, and show all the steps you use to get a solution. Incidents of cheating on homework or exams receive failing grades and are subject to Departmental disciplinary action. Due dates of HW and exam dates can be found in the daily schedule attached to the syllabus. Other class information will be announced by email and on Blackboard so please check your email and Blackboard regularly.

Homework will be graded under the following guidelines:
1) Each assignment is worth 25 points
2) Your lowest homework score will be dropped
3) Units are required with all answers.
4) Homework must have the following on the header: EE2120, assignment number, name, and section.
   Homework is to be neat, legible and stapled. Homework without the proper header or staple may have 4 points deducted from the assignment.

Grading Policies: Grades will be given on the 90%, 80%, and 70% scale for A, B, and C letter grades.

Online Material: Video lectures are available on Blackboard. Blackboard quizzes are due before class on the day listed in the daily schedule. No quizzes will be dropped. Quizzes and bonus problems will be included in the homework grade.

Class Attendance: Attendance is compulsory. Attendance will be taken and be considered in borderline cases. Attendance will be taken at the beginning of class, late is considered absent (after 5 minutes).

Exam Policies: Make-up exams will not be given. Missing an exam will result in a zero for the exam. You may replace your lowest exam score with your percentage on the final exam. If you will need to miss an exam make arrangements before the exam date to take the exam early.

Grade Distribution: Homework (10%), Tests (60%), Final (30%)

Final Examination: Friday, July 29, 2016, 8:00 a.m.-10:00 a.m. The EE2120 final must be passed with a C or better to pass the course with a C or better.

Course Outline:
   Appendix B (Complex Algebra), Chapter 10 (Phasors), Chapter 11 (AC Circuit Analysis)
   Test 1 - Thursday, June 23, 2016
   Chapter 12 (AC Power Analysis), Chapter 13 (Frequency Response Analysis)
   Test 2 - Tuesday, July 12, 2016
   Chapter 17 (Mutual Inductance and Ideal Transformers), Chapter 18 (3-Phase Power Systems)
   Test 3 - Tuesday, July 26, 2016
   Review and Final

Linear Solvers: There are certain abilities that can speed up the process of the mathematics that are used to analyze the problems we will cover. One such ability that will save time is the use of a linear solver on a calculator. I will expect that you will be able to do this. Most current calculators can solve these systems in complex form which can lend a great deal to the analysis of circuits.

Emergency Safety Exits: Egress maps for all areas of instruction on campus are available on the web at:
http://registrar.mst.edu/links/egress.html. Students are encouraged to review this site and be aware of the emergency exit signs near their classrooms.